|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | FPGA | AFTAB | | AFTAB+PLA+STACK | | PLA+STACK | | |
| Resource | Available | Utilization | Utilization% | Utilization | Utilization% | Overhead | Overhead% | FPGA Utilization% |
| LUT | 63400 | 2860 | 4.51104101 | 3149 | 4.96687697 | 289 | 10.1048951 | 0.455836 |
| FF | 126800 | 2602 | 2.05205047 | 3505 | 2.76419558 | 903 | 34.7040738 | 0.7121451 |

Xilinx - Artix-7 - xa7a100tcsg324-2i